



AEIT 2024

Trento, 25/27 Settembre 2024

L'Italia in Europa: semiconduttori e fotonica

Chips Mirror Group Italy

Livio Baldi



L'Italia in Europa: semiconduttori e fotonica

15:00-16:00

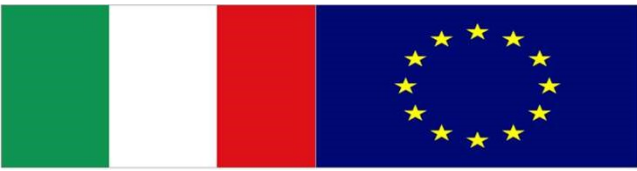
- **Special Session L'Italia in Europa: semiconduttori e fotonica**
Chair: Livio Baldi - AEIT-AMES
- **Introduzione Chips Mirror Group Italy** Livio Baldi - AEIT-AMES
- **New opportunities for the Italian Semiconductor Sector: The Foundation Chips-IT and the WBG Pilot Line** Enrico Sangiorgi - Fondazione Chips.it
- **Investimenti industriali** Gabriele Silvestri – Invitalia

16:00-17:00

- **Special Session L'Italia in Europa: semiconduttori e fotonica**
Chair: Roberta Ramponi, CORIFI

Co-organized by:





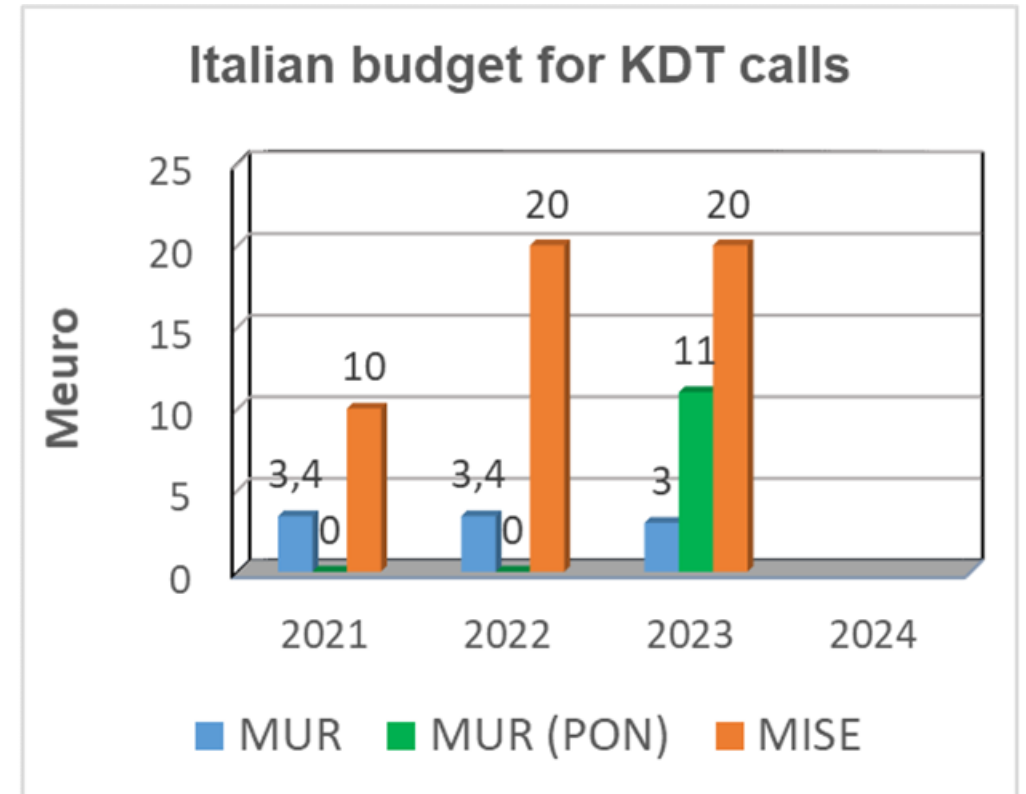
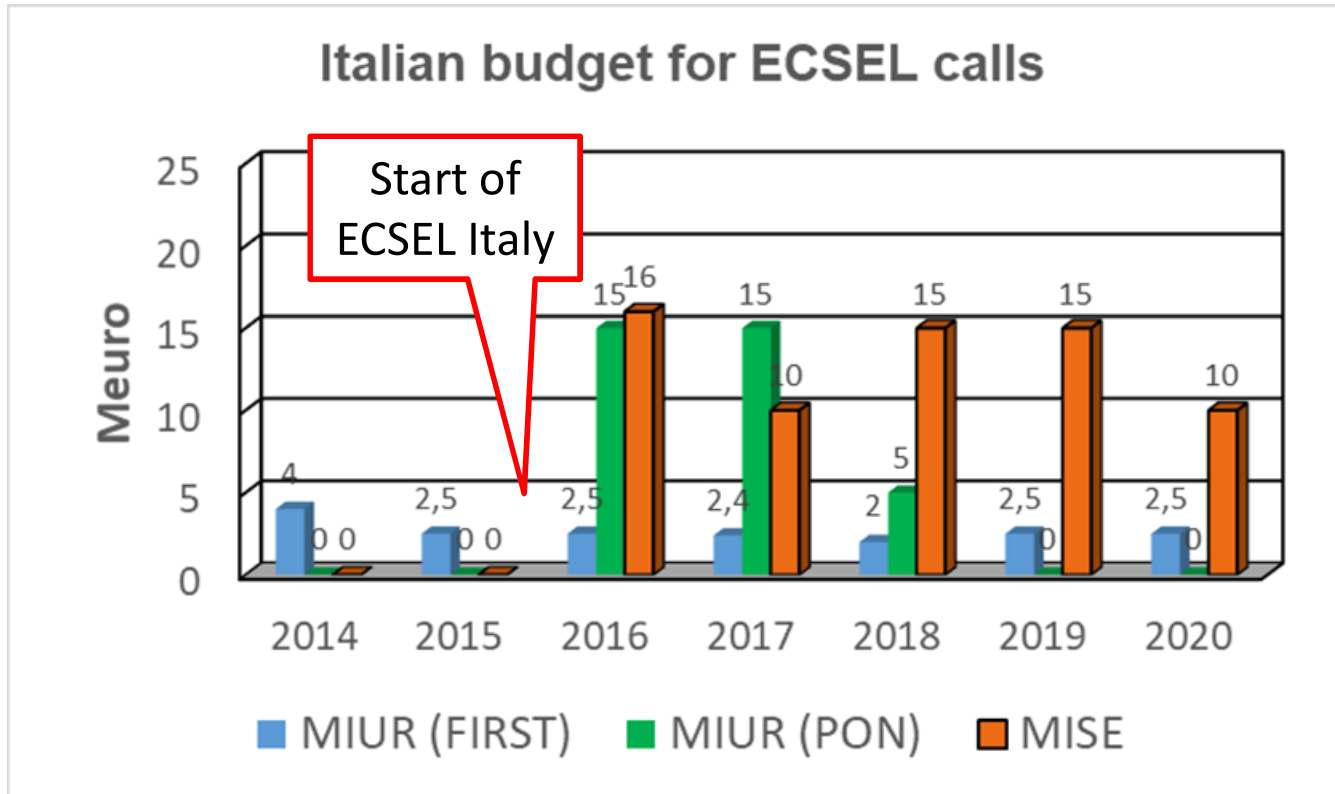
what is it?



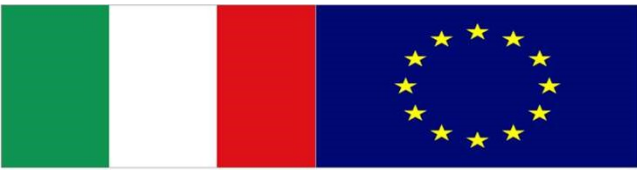
- Chips Mirror Group Italy is the continuation of the ECSEL Mirror Group Italy: an organization including researchers from Italian industrial and academic institutions involved in the **Chips JU** programme:
 - ✓ To facilitate / coordinate the participation of Italian industrial and academic research organizations to the Chips Joint Undertaking (JU)
 - ✓ To promote the support by Italian Public Authorities,
 - ✓ To coordinate with similar initiatives in Europe.



Italian budget in ECSEL and KDT JU



- In the period 2014-2020 (ECSEL JU), a total of 119.4 M€ were made available by MUR and MISE. For the KDT JU, 50 M€ from MIMIT, 9.8 M€ from MUR and 11 from PON funds.
- MUR and MIMIT have made available 22 M€ for the first calls of the Chips JU



2022: European Chips Act



European Semiconductor Board (Governance)

Chips JU:

- Calls
- Pilot Lines
- Competence Centers

Pillar 1

Chips for Europe Initiative

- Initiative on infrastructure building in synergy with the EU's research programmes
- Support to start-ups and SMEs

Pillar 2

Security of Supply

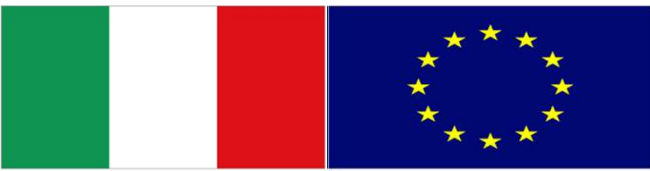
- First-of-a-kind semiconductor production facilities

Attract new fabs in Europe

Pillar 3

Monitoring and Crisis Response

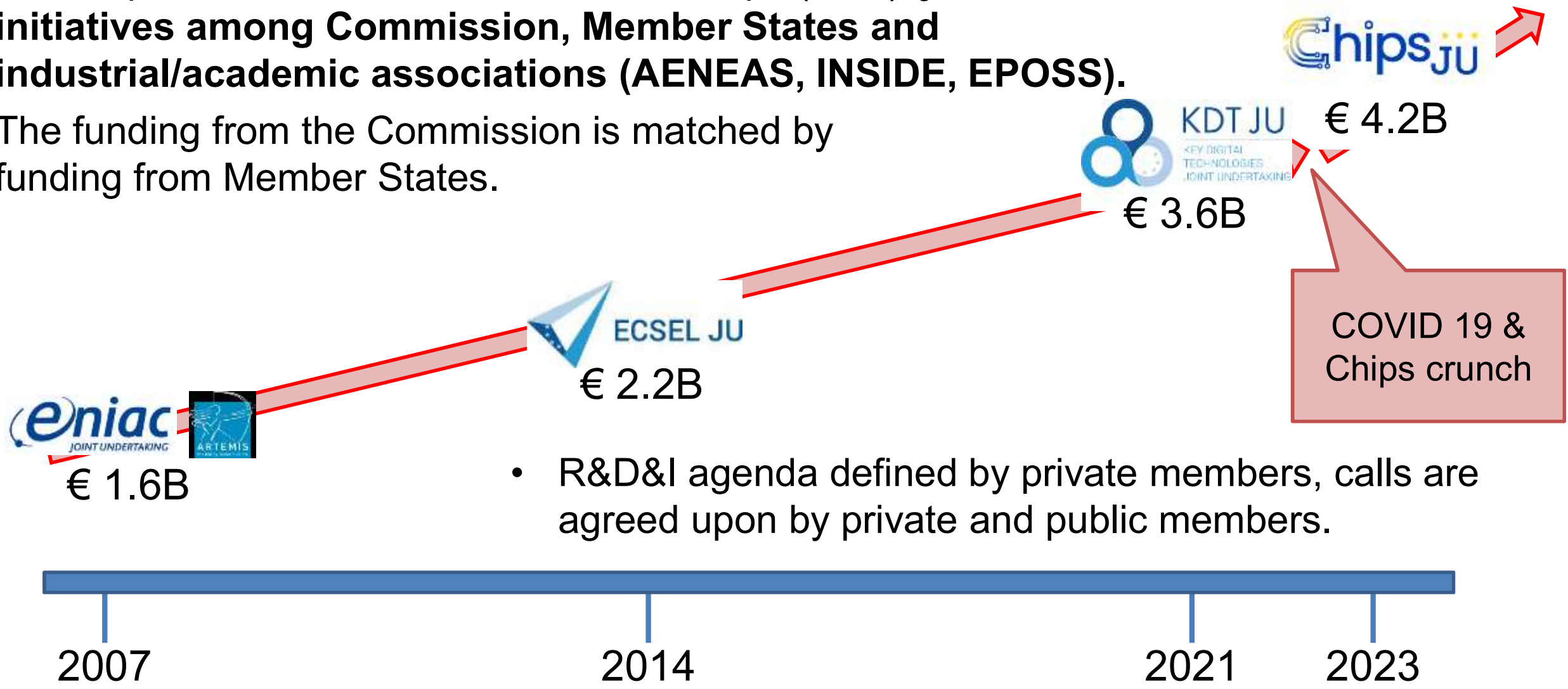
- Monitoring and alerting
- Crisis coordination mechanism with MS
- Strong Commission powers in times of crisis



The Joint Technology Initiatives (JTI)



- JTI are part of the Public-Private Partnerships (PPP): **joint R&D&I initiatives among Commission, Member States and industrial/academic associations (AENEAS, INSIDE, EPOSS).**
- The funding from the Commission is matched by funding from Member States.



- R&D&I agenda defined by private members, calls are agreed upon by private and public members.



What can we do for you?

- Disseminate information on Chips JU events and calls.
- Keep informed on future Chips JU and Chips Act initiatives and on running project proposals
- Collect project ideas and presentation of partners' competences for future brokerage events.
- Provide support and advice in the search for projects partners and in proposal preparation.





Chips Mirror Group Italy Structure



- Chips Mirror Group Italy, like ECSEL Mirror Group Italy, has the administrative support of AEIT-AMES.
- Funding has been insured by participation in H2020 and Horizon Europe CSA's.

2017-2018



In-depth support for innovation and exploitation in
Smart Systems Integration

2019-2021

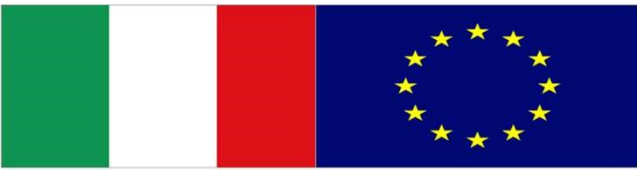


Federating European Electronics Ecosystems for
Competitive Electronics Industries

2023-2025



International Cooperation On Semiconductors



The ICOS Project

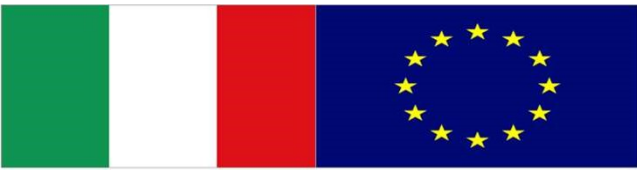


ICOS Project is a Cooperation & Support Action started in January 2023 for three years, funded by the Horizon Europe under contract N.101092562, with the purpose:

To identify and support the establishment of **scientific international collaborations** with like-minded countries, and to promote the growth of the European Semiconductor industry through **focused research alliances**.

PARTNERS

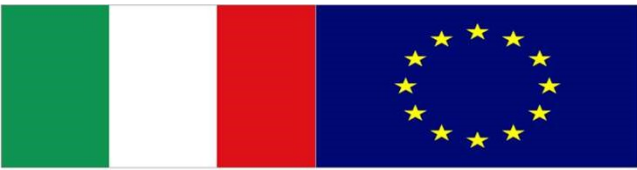
ACADEMICS	RTOS	ASSOCIATIONS & CONSULTING COMPANIES	INDUSTRIALS



Chips Mirror Group Italy in:



- The tasks of Chips Mirror Group Italy in the project are:
 - Collect inputs from Italian industrial associations on semiconductors;
 - Organize at least **3 meetings** aiming at disseminating the results of the project to the industrial and scientific community.
 - Disseminate the project to the linked ECSEL/KDT/Chips JU Mirror Groups in other European countries.
- Already two dedicated meetings have been held in **association with the yearly meetings of SIE**, Associazione Società Italiana di Elettronica



A new web site



In addition to the previous web site,
hosted by AEIT at:

<https://www.aeit.it/ecsel>,

a new web site is available at:

<https://www.chips-mgi.it/>





Thank you!

<http://www.aeit.it/ecsel>

Contacts

- baldi.livio@gmail.com (President)
- enrico.sangiorgi@unibo.it (Vice-chairman University)
- roberto.zafalon@st.com (Vice-chairman Industry)

Steering Committee

